

200	202	Subclass
711	Class	ISSUE CLASSIFICATION

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PATENT NUMBER

U.S. UTILITY Patent Application

RA	O.I.P.E.	PATENT DATE
SCANNED	EWY	O.A. CR

APPLICATION NO. 09/652023	CONT/PRIOR F	CLASS 365 711	SUBCLASS 202	ART UNIT 2610 2824	EXAMINER W. Choi M. Kim
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APPLICANTS
Yasuhiro Wakimoto

TITLE
Microprocessor and memory device

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
711	202	711	3	125	125
INTERNATIONAL CLASSIFICATION					
G 06 F 11/00					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	5	5	5	25	1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	Woo H. Choi 1/12/04 (Assistant Examiner)			NOTICE OF ALLOWANCE MAILED 1-13-04	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	J. Baier 1/16/04 (Primary Examiner)			ISSUE FEE Amount Due \$ 1330.00 Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	O. Barnes 1/16/04 (Legal Instruments Examiner)			ISSUE BATCH NUMBER	

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(Attached in pocket on right inside flap)Formal Drawings (____ sheets) 00

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